



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Prashant Sethi, et al.

Serial No.: 10/806,787

Filed: March 22, 2004

For: A Method And System For Configuration
Of Processor Integrated Devices In Multi-
Processor Systems

§ Group Art Unit: 2181

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§ Examiner: Chun Kuan Lee

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§ Atty. Dkt. No.: ITL.1576US
(P18870)

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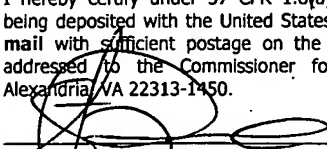
AMENDMENT SUBMITTED WITH RCE

Sir:

This paper is submitted in conjunction with the enclosed Request for Continuing Examination (RCE) in the above-identified Patent Application. Further examination and reconsideration are respectfully requested in view of the Amendments and Remarks set forth below.

Amendments to the Claims begin on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.

Date of Deposit: <u>12/6/07</u>
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.
 Stephanie Petreas